### Paper Template for VLSI Design/CAD Symposium 2020

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#### Abstract

Put your abstract here.

**1. Introduction**

Put the paper introduction here.

**2. Important Rules**

The paper is limited to 2 to 4 pages, including images. Please do not paginate your paper.

The first paragraph in each section should not be indented, but all following paragraphs within the section should be indented as these paragraphs demonstrate.

Page size is A4. In formatting your A4-size paper, set top margin to 20 mm (0.8 inches), bottom margin to 25 mm (1 inch), left margin to 14 mm (0.56 inches) and right margin to 15 mm (0.6 inches). The column width is 88 mm (3.46 inches) with 5mm (0.2 inches) space between the two columns. All text must be in a two-column format and the font size should be larger than 9pt. The space between these two columns is 75mm.

Please use vector graphics instead of bitmap pictures for illustrations to achieve high paper quality.

Please embed all the used fonts inside the PDF file. The fonts for the text part should be Times.

For the reference part, please follow the rules of IEEE [1][2].

**2.1. Subheadings**

Subheadings should appear in lower case (initial word capitalized) in boldface.

*2.1.1. Sub-subheadings*

Sub-subheadings, as in this paragraph, are discouraged.

**3. Conclusion**

Put your conclusion here.

**4. References**

1. M. Koyanagi, T. Fukushima, and T. Tanaka, “High-density through silicon vias for 3-D LSIs,” *Proceedings of the IEEE*, vol. 97, no. 1, pp. 49–59, Jan. 2009.
2. M. Nicolaidis, N. Achouri, and L. Anghel, “A diversified memory built-in self-repair approach for nanotechnologies,” in *Proc. IEEE VLSI Test Symp. (VTS)*, Napa Valley, Apr. 2004, pp. 313–318.